



HAPS_CMI

ARM® Integrator/CM Interface

Part of the Confirma™ ASIC/ASSP Verification Platform



Building hardware prototypes of ASIC designs containing ARM® processors is easy with HAPS_CMI. HAPS_CMI acts as a link between ARM Core Modules and a HAPS system. Just let the ARM processors run on ARM Integrator/CM boards and the rest of the ASIC design on a HAPS system.

Features

- Board form factor is larger than a “short” 3x2 board, to allow the Europe sized ARM Integrator/CM board to fit as a daughter board
- Designed to mate with the two extension connectors HDRA and HDRB on an ARM Integrator/CM board
- One or two ARM Integrator/CM boards can be stacked on top of HAPS_CMI
- The ARM Integrator/CM boards can be clocked from a global HAPS output clock or from the HAPS connector
- All signals from HDRA and HDRB are connected to the HAPS connector
- 21 remaining HAPS signals are available in standard 0.1” pitch headers for probing and GPIO
- Signals can be probed with existing Mictor connectors on the ARM Integrator/CM boards
- All signal levels are 3.3 V

Supported Cores

ARM720T	ARM926EJ-S	ARM1020E
ARM7TDMI	ARM940T	ARM1026EJ-S
ARM920T	ARM946E-S	ARM1136JF-S
ARM922T	ARM966E-S	

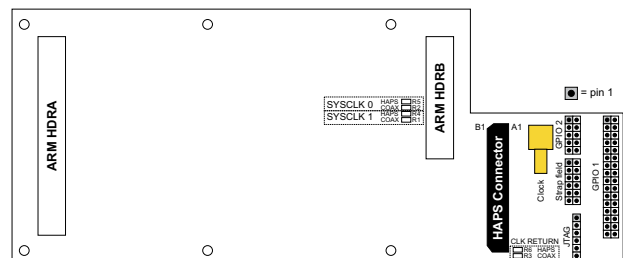
Clocks

A coax connector mounted on the board can be connected to a HAPS global clock output. 0-Ohm resistors select the source of each of the SYSCLK[1:0] signals from this coax input or from a signal in the HAPS connector. Termination resistors for each clock can be mounted on the board if needed. Any of the two clocks can also be connected to the HAPS connector clock input to provide a feedback path for the clock generated on the HAPS motherboard.

JTAG

Provisions are made so that the JTAG chain on the ARM Integrator/CM board(s) can be inserted in the HAPS motherboard JTAG chain, or separated from it by moving straps in a strap field.

Board Layout



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