



CON_CABLE40

Interconnect Cable

Part of the Confirma™ ASIC/ASSP Verification Platform



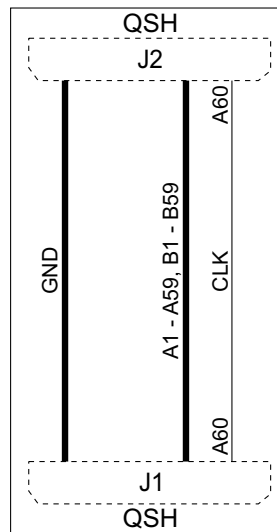
CON_CABLE connects all signals in two non-adjacent connectors with controlled wire impedance. The primary use is for adding connections between FPGAs or two motherboards.

CON_CABLE has straight clock connections (A59 - A59, A60 - A60) and no connection between the power pins B60.

Features

- Length: 419 mm
- QSH-060-01-L-D-A connector in both ends
- 50 Ohm ribbon coax cable
- Adjacent pins in the connectors may host differential (LVDS) signals
- Power pins (B60) are not connected
- Signal delay < 4.0 ns

Block Schematic



Other Interconnect Boards

Besides the CON_CABLE40, other interconnect boards and cables are available:

- CON_2x1 2-way bus in the transversal direction
- CON_2x1A as CON_2x1 with additional features
- CON_2x1B as CON_2x1A with additional features
- CON_2x2 4-way bus, or two diagonal buses
- CON_1x2 2-way bus in the longitudinal direction
- CON_1x1 vertical bus; available in four different heights
- CON_CABLE40X 2-way bus; 40 cm long extension cable
- CON_CABLE_RIO RocketIO bus; for non-adjacent connectors
- CON_RIO RocketIO bus; for adjacent connectors



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